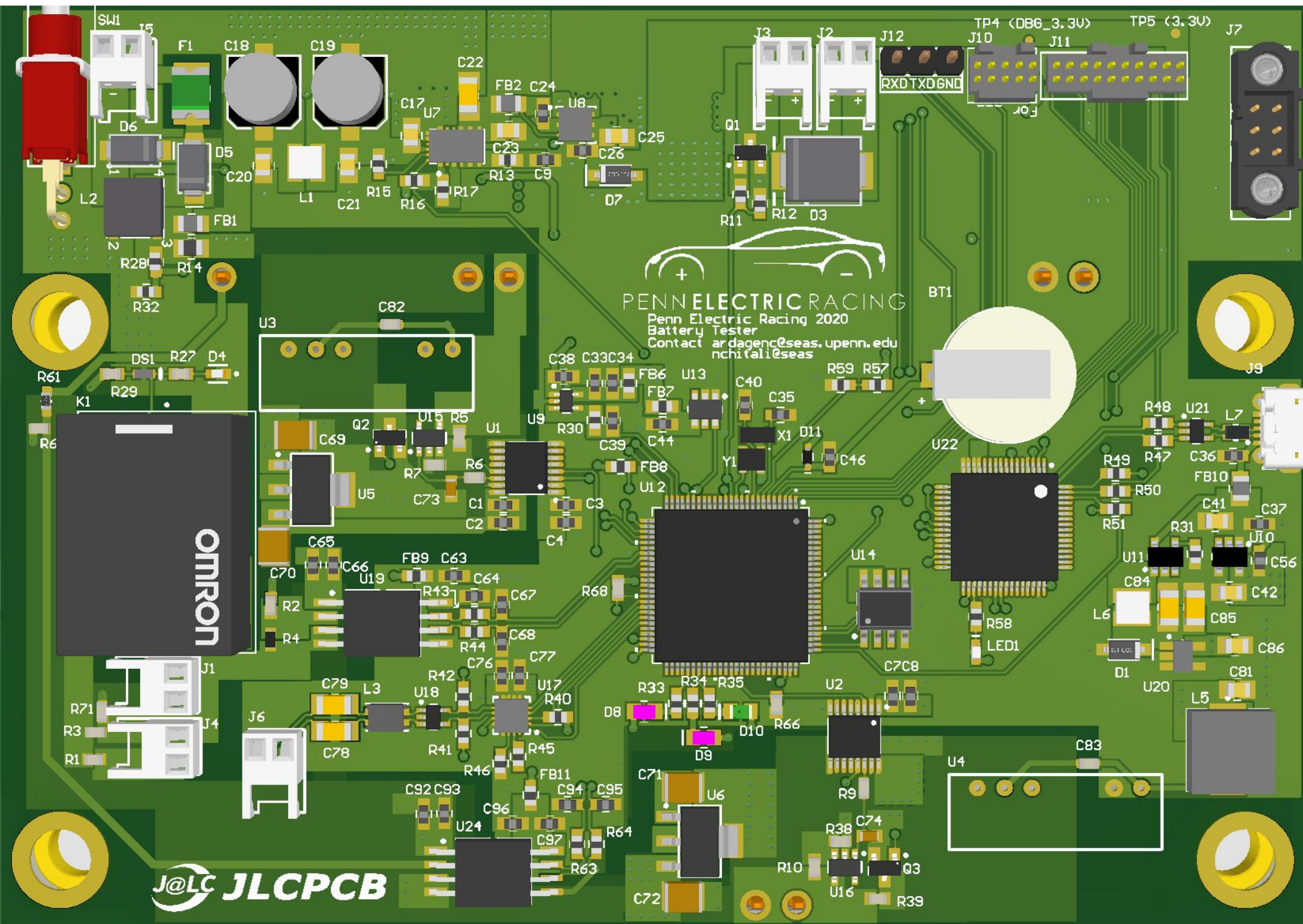
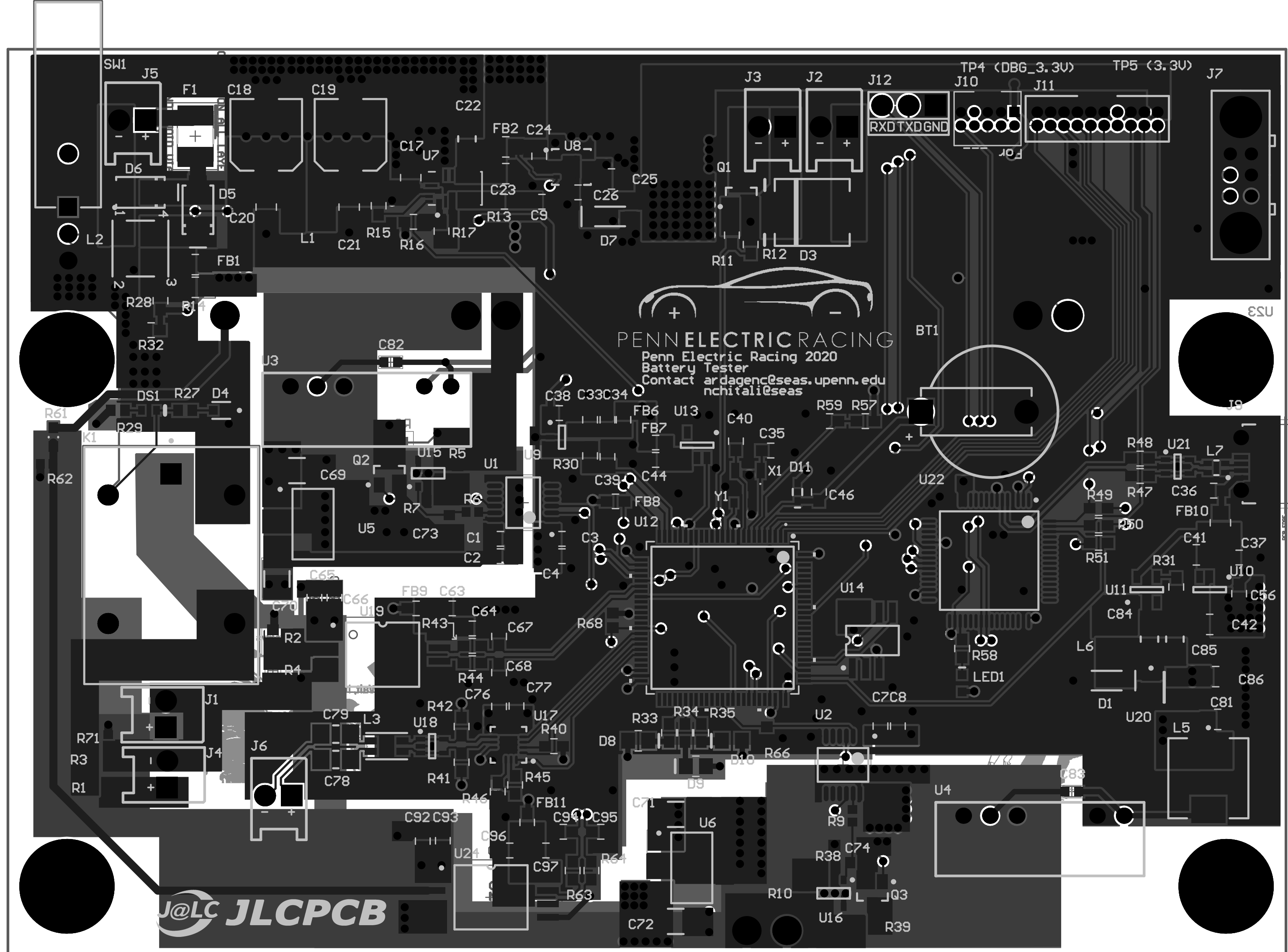
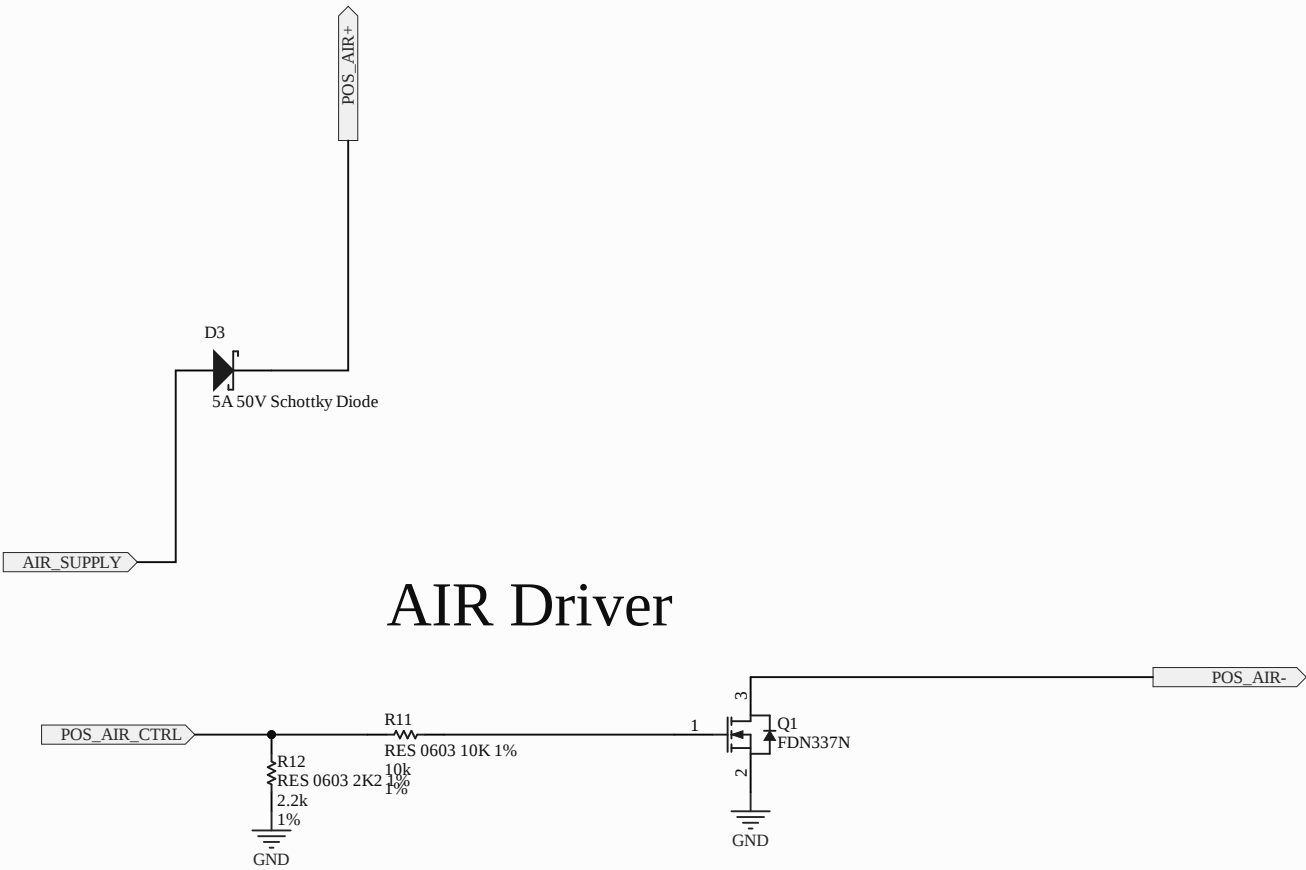
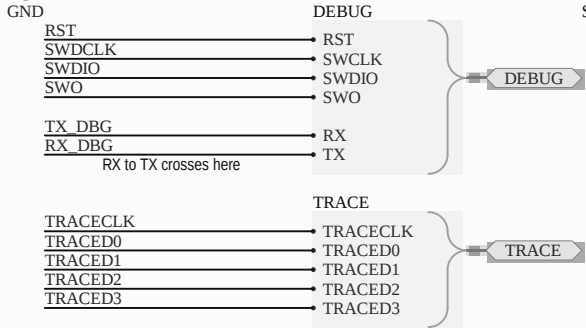
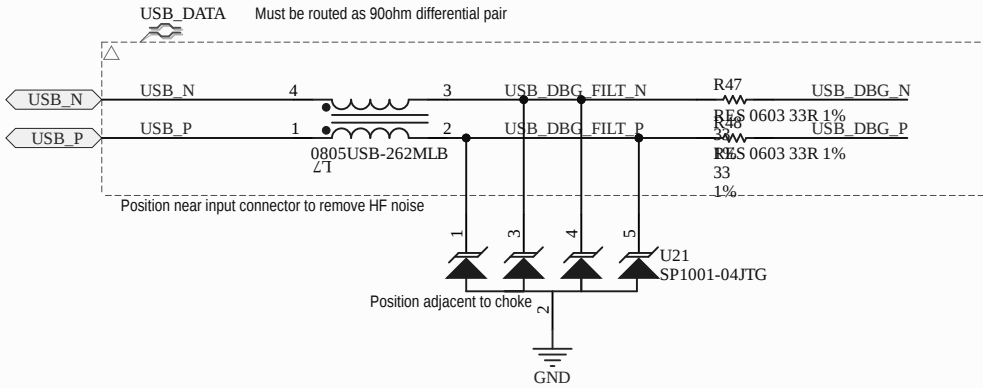
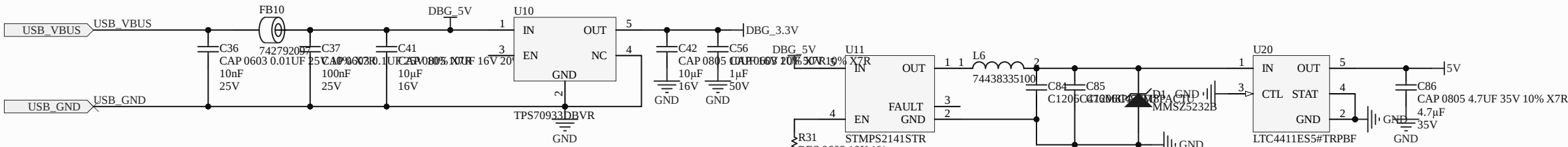




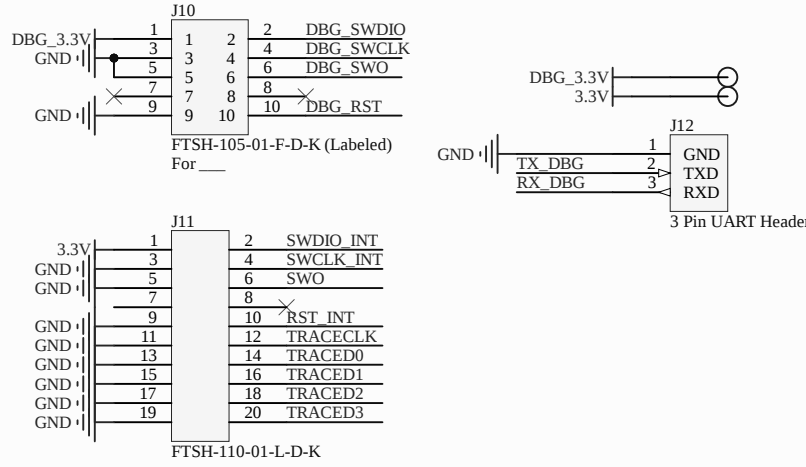
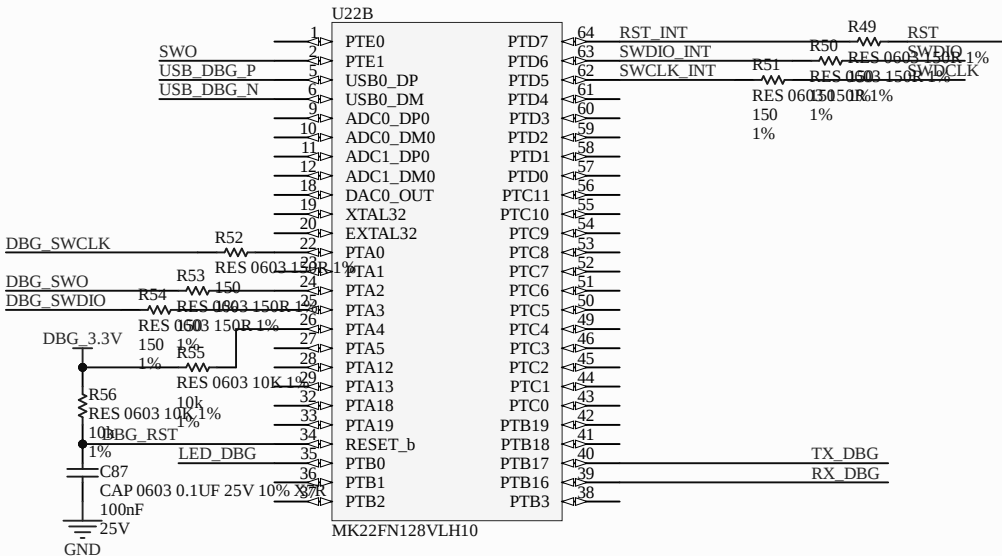




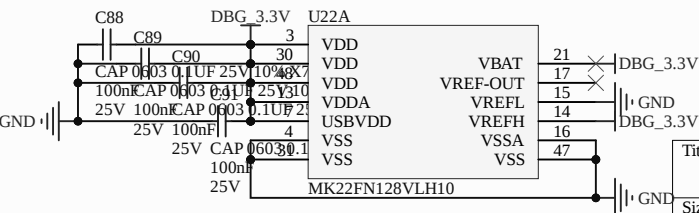
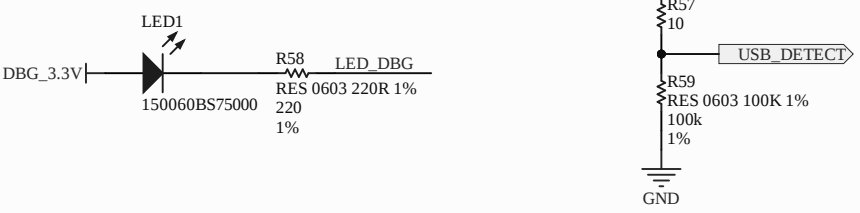
Use thick traces/polygons/planes for USB power and ground, the core AMS should be able to run from USB power (minus heavy loads like fans and TSAL).



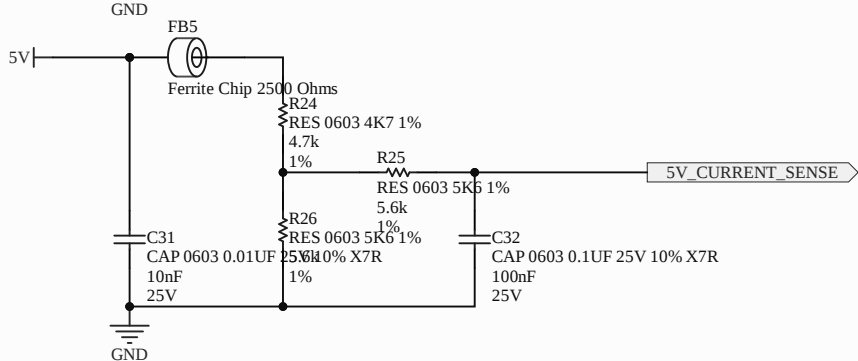
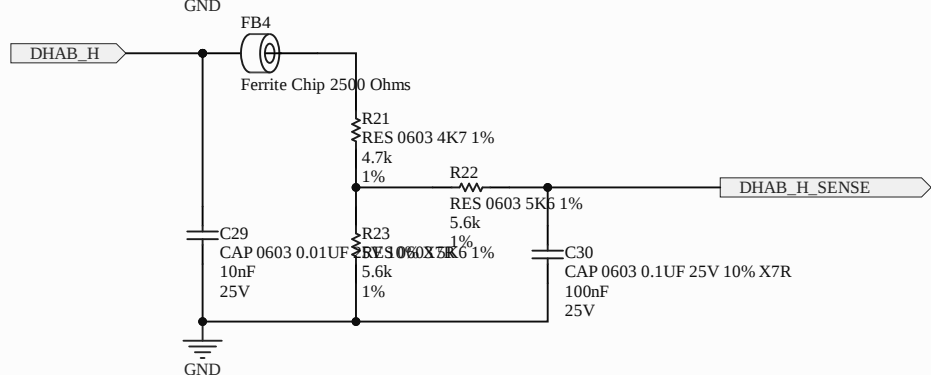
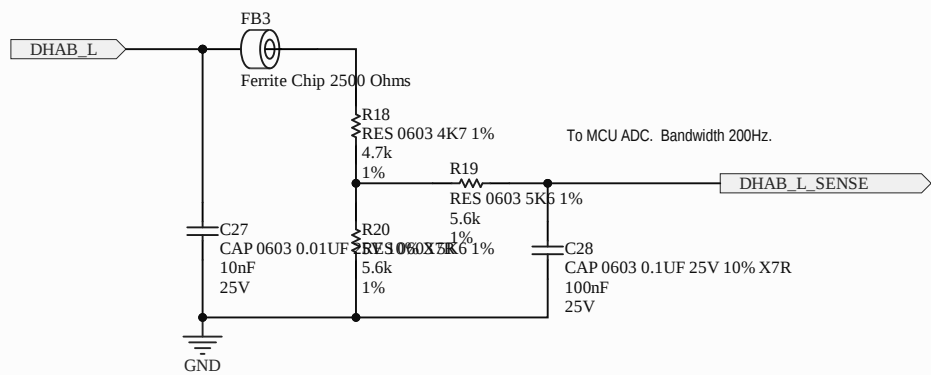
STAT pin may be left floating or grounded when not in use (page 4)
CTL pin tie to GND to enable



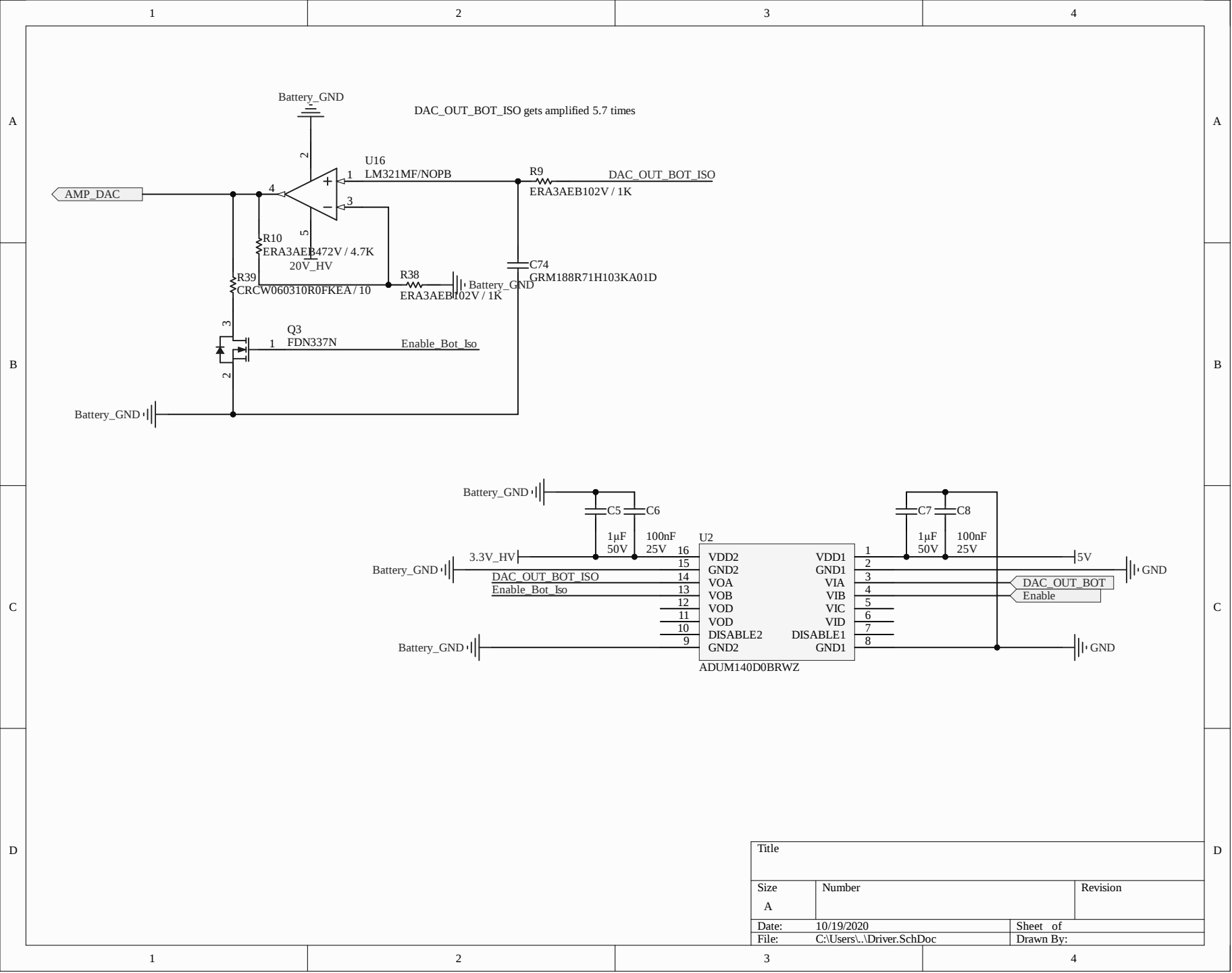
Hold debugger in reset unless 5V usb cable plugged in

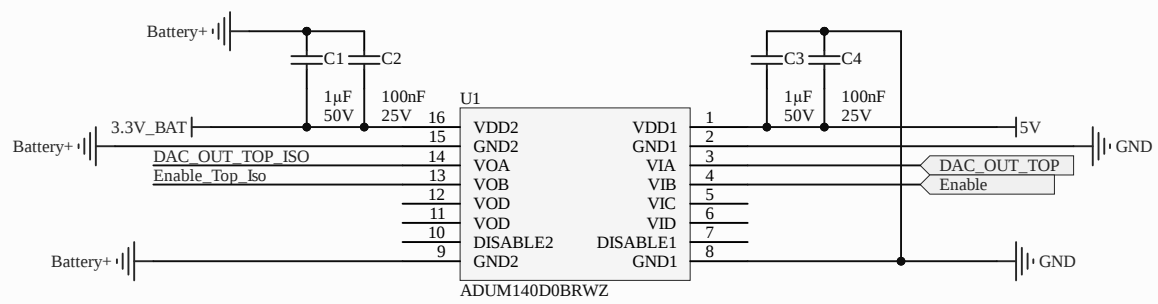
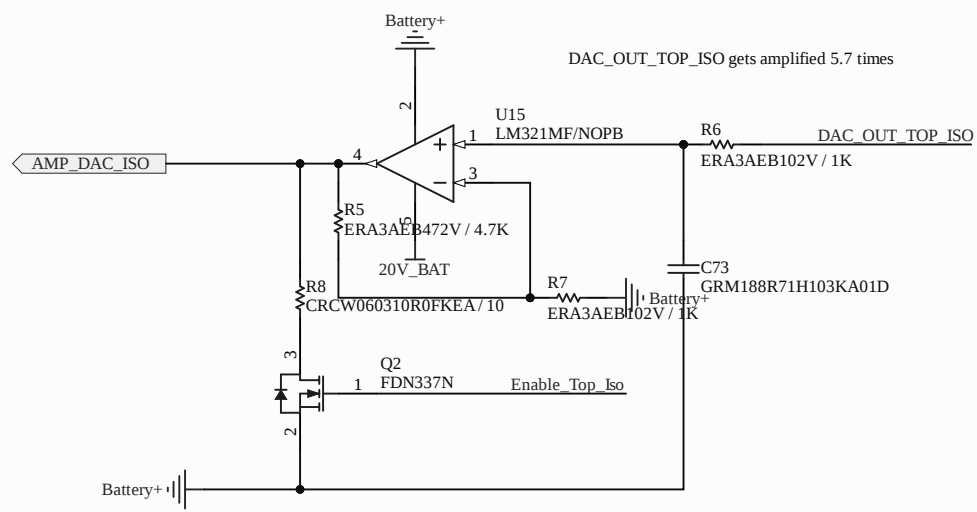


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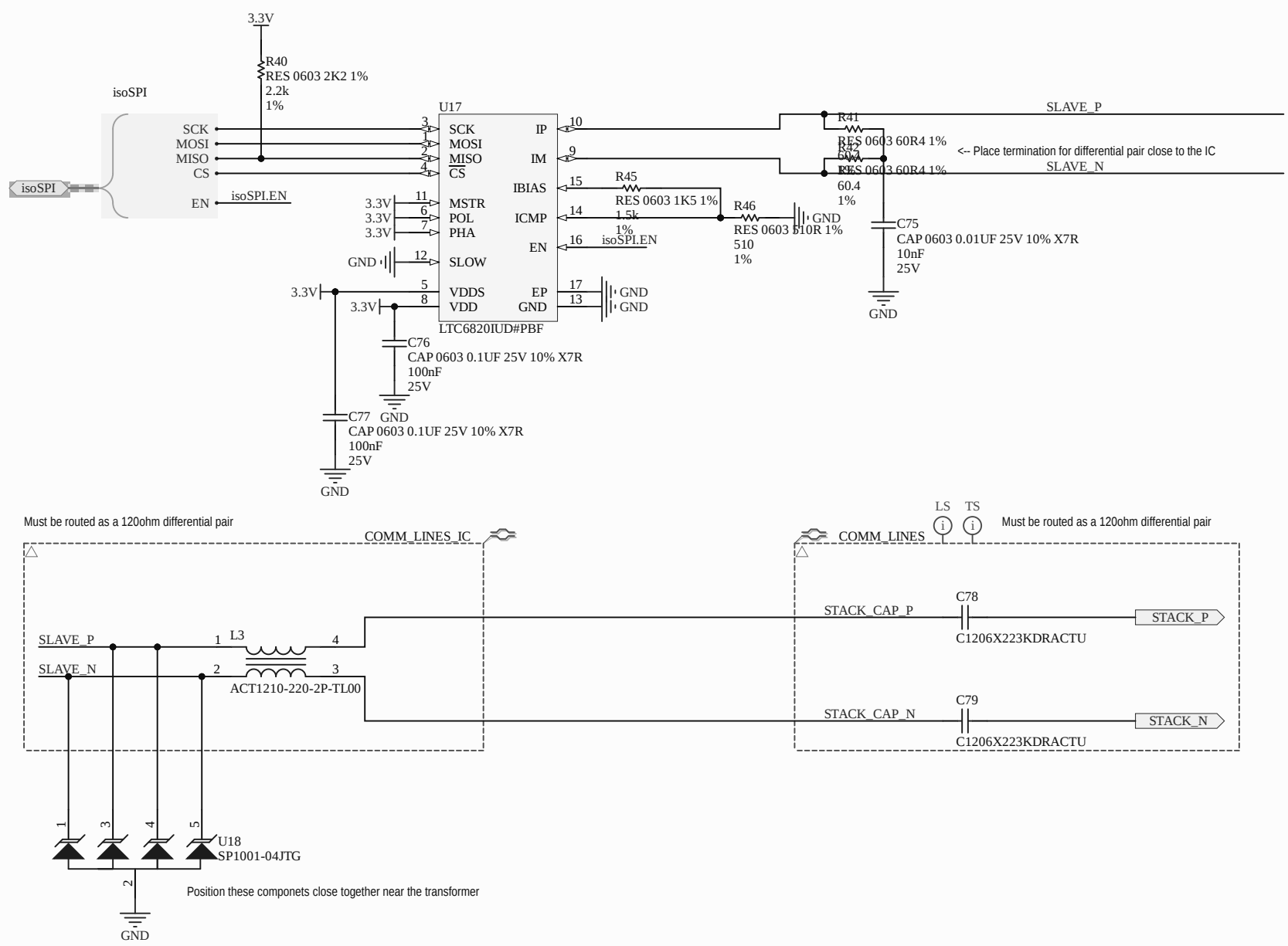


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